

Case Name. Serial No. Applicant:

J.A. Garceran 2-12-20

09/784846

Applicant: J.A. Garceran, et al. Filing Date: February 16, 2001

Group: | 2681

U.S. PATENT DOCUMENTS

| *Examiner Initial | | Document Number | Date | Name | Class | Subclass | Filing Date |
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FOREIGN PATENT DOCUMENTS

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| AG | | | | | | |
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OTHER (including Author, Title, Date, Pertinent Pages, etc.)

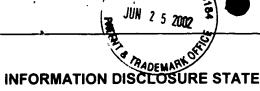
| -37 | Al | Analog Devices, "High Speed Design Techniques, 9/96, pp. 6-25 to 6-30 |
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^{***}References listed beyond AZ would list as AA-1, AB-2, AC-3 thru AZ-26.

| EXAMINER | 1 mille | DATE CONSIDERED |
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^{*}Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant

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INFORMATION DISCLOSURE STATEMENT

Case Name. J.A. Garceran 2-12-20 Serial No.

09/784846

Applicant: Filing Date:

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Group:

2681

U.S. PATENT DOCUMENTS

| *Examiner - Initial | | Document Number | Date | Name | Class | Subclass | Filing Date |
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| 778 | AE | EP 0 534 255 A2 | 3/31/93 | EPC | H04L | 5/06 | Yes |

OTHER (including Author, Title, Date, Pertinent Pages, etc.)

| | 30 | AF | European Search Report dated May 13, 2002. |
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^{***}References listed beyond AZ would list as AA-1, AB-2, AC-3 thru AZ-26.

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